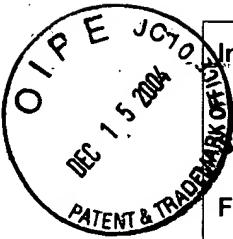


12-16-004

2188  
CJW/BS



In re application of:	Thomas C. McDonald Terry Parks
Serial No.:	09/849800
Filed:	05/04/01
Docket:	CNTR.2063
For:	APPARATUS AND METHOD FOR TARGET ADDRESS REPLACEMENT IN SPECULATIVE BRANCH TARGET ADDRESS CACHE

**INFORMATION DISCLOSURE STATEMENT  
SUBMITTED UNDER 37 CFR 1.97(D) AND 1.97 (E)(2)**

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

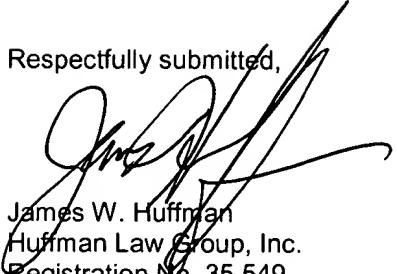
It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement. The information cited in the information disclosure statement was cited by the United States Patent Office in one or more applications that are related to the present application.

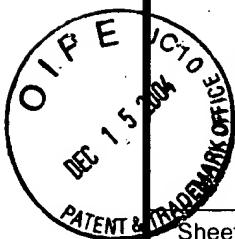
Also attached hereto is a check in the amount of \$180.00 for the fee set forth in § 1.17(p).

Respectfully submitted,

  
James W. Huffman  
Huffman Law Group, Inc.  
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Date: 12-10-04

"EXPRESS MAIL" mailing label number E0003400670013  
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Patents and Trademarks, Washington, D.C. 20231.  
By: James W. Huffman



Substitute for form 1449A/PTO				<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	09/849800
				Filing Date	05/04/01
				First Named Inventor	Thomas C. McDonald Terry Parks
				Group Art Unit	2188
				Examiner Name	Thang H. Ho
Sheet	1	of	2	Attorney Docket Number	CNTR.2063

## **U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup>			
	AA	5142634		Fite et al	08/25/1992	
	AB	5163140		Stiles et al	11/10/1992	
	AC	5353421		Emma et al	10/04/1994	
	AD	5355459		Matsuo et al	10/11/1994	
	AE	5530825		Black et al	06/25/1996	
	AF	5850543		Shiell et al	12/15/1998	
	AG	5867701		Brown et al	02/02/1999	
	AH	5948100		Hsu et al	09/07/1999	
	AI	5964868		Gochman et al	10/12/1999	
	AJ	5974543		Hilgendorf et al	10/26/1999	
	AK	6044459		Bae et al	03/28/2000	
	AL	6108773		Col et al	08/22/2000	
	AM	6151671		D'Sa et al	11/21/2000	
	AN	6314514		Thomas C. McDonald	11/6/2001	
	AO	6601161		Rapoport et al	07/29/2003	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST .3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST .16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

**Burden Hour Statement:** This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Substitute for form 1449A/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	09/849800
(use as many sheets as necessary)				Filing Date	05/04/01
				First Named Inventor	Thomas C. McDonald Terry Parks
				Group Art Unit	2188
				Examiner Name	Thang H Ho
Sheet	2	of	2	Attorney Docket Number	CNTR.2063

### OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	BA	SAKAMOTO ET AL; <u>Microarchitecture Support for Reducing Branch Penalty in a Superscaler Processor</u> ; pp. 208-216; IEEE 1996; Mitsubishi Electric Corp., System LSI Laboratory, 4-1 Mizuhara, Itami, Hyogo 664, Japan.	
	BB	YEH ET AL; <u>ALTERNATIVE IMPLEMENTATION OF TWO-LEVEL ADAPTIVE BRANCH PREDICTION</u> ; 19 <sup>TH</sup> Annual International Symposium on Computer Architecture, pp. 124-134, May 19-21, 1992, Gold Coast, Australia.	
	BC	CHANG ET AL; <u>ALTERNATIVE IMPLEMENTATIONS OF HYBRID BRANCH PREDICTORS</u> ; Proceedings of MICRO-28, 1995, IEEE.	
	BD	MC FARLING, SCOTT; <u>WRL TECHNICAL NOTE TN-36</u> , "Combining Branch Predictors, June 1993, Western Research Laboratory, 250 University Ave., Palo Alto, CA 94301	
	BE	IEEE 100; <u>THE AUTHORITATIVE DICTIONARY OF IEEE STANDARDS TERMS</u> ; Seventh Edition, IEEE, Standards Information Network, IEEE Press,	
	BF	BRAY ET AL; <u>STRATEGIES FOR BRANCH TARGET BUFFERS</u> ; Technical Report No. CSL-TR-91-480, June 1991.	

Examiner Signature		Date Considered	
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